

Case U.S. PTO
10/028766
12/28/01

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10028766	12/28/2001	257		2811	A. J. J. J.

**APPLICANTS: Shimizu Satoshi; Oda Hidekazu;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 08/874,410 06/13/1997

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** FOREIGN APPLICATIONS VERIFIED:

JAPAN 9-21531 02/04/1997

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

216780US-2 DIV

TITLE : Mis transistor and method of fabricating the same

U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
Amount Due	Date Paid		Total Claims	Print Claim for 0.6
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drawg.	Figs. Drawg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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